

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1165	324/763.ccls.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:43
L2	0	1 and control and (test\$3 near3 enabl\$4) and (differential near3 driver) and (input near3 node) and (output near3 node) and amplif \$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3)) and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:53

L3	0	1 and (test\$3 near3 enabl\$4) and (differential near3 driver) and (input near3 node) and (output near3 node) and amplif \$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3))) and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:54
L4	0	1 and (differential near3 driver) and (input near3 node) and (output near3 node) and amplif \$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3))) and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:54

L5	0	1 and (differential near3 driver) and amplif\$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3)) and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:54
L6	0	1 and driver and amplif\$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3)) and (program\$6 near3 impedance) and receiver.clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:55
L7	0	1 and amplif\$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (preamplif\$4 or pre\$amplif\$4) and (current near3 (dac or (digital\$4 near3 analog\$3)) and (program\$6 near3 impedance).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:55

L8	0	1 and amplif\$4 and ((fir or (finite near3 impuls\$3 near respons\$5)) near3 latch\$4) and (current near3 (dac or (digital\$4 near3 analog\$3)) and (program\$6 near3 impedance).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:55
L9	0	1 and control and (resistor or resistance) and switch\$3 and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:56
L10	0	1 and control and (differential near3 driver) and (resistor or resistance) and switch\$3 and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:57
L11	0	1 and (differential near3 driver) and (resistor or resistance) and switch\$3 and (program\$6 near3 impedance) and (differential near3 receiver). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:57

L12	2	1 and driver and (resistor or resistance) and switch\$3 and (program\$6 near3 impedance) and receiver.clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:57
L13	0	1 and control and (differential near3 driver) and (program\$6 near3 impedance) and (differential near3 receiver) and amplif\$4 and ((offset near3 voltage) or comparat\$4) and (level near3 shift \$3) and (output near3 stage).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:59
L14	0	1 and control and (differential near3 driver) and (program\$6 near3 impedance) and (differential near3 receiver) and amplif\$4 and ((offset near3 voltage) or comparat\$4) and (level near3 shift \$3).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 16:59
L15	0	1 and control and driver and (program\$6 near3 impedance) and receiver and amplif\$4 and ((offset near3 voltage) or comparat\$4) and (level near3 shift \$3).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 17:07

L16	0	1 and control and driver and (program\$6 near3 impedance) and receiver and amplif\$4 and ((offset near3 voltage) or comparat\$4) and shift\$3.clm.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 17:08
L17	0	1 and driver and (program\$6 near3 impedance) and receiver and amplif\$4 and ((offset near3 voltage) or comparat\$4) and shift\$3.clm.	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/17 17:08

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